

INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Not for submission under 37 CFR 1.99)</i>	Application Number	10001478
	Filing Date	2001-11-01
	First Named Inventor	Craig Nemecek
	Art Unit	2123
	Examiner Name	Jason Scott Proctor /Dwin Craig/
	Attorney Docket Number	CD01213M

U.S.PATENTS						
Examiner Initial*	Cite No	Patent Number	Kind Code ¹	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
/D.C./	1	6910126	B1	2005-06-21	Mar et al.	Entire Document
/D.C./	2	6825689	B1	2004-11-30	Snyder	Entire Document
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/D.C./	6	6892322	B1	2005-05-10	Snyder	Entire Document
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Examiner Name	Jason Scott Proctor	/Dwin Craig/
Attorney Docket Number	CD01213M	

/D.C./	9	6611220	B1	2003-08-26	Snyder	Entire Document
/D.C./	10	7180342	B1	2007-02-20	Shutt et al.	Entire Document
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/D.C./	15	6854067	B1	2005-02-08	Kutz et al.	Entire Document
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Examiner Name	Jacob Scott Preeter /Dwin Craig/	
Attorney Docket Number	CD01213M	

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/D.C./	27	7092980	B1	2006-08-15	Mar et al.	Entire Document
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/D.C./	1	USPTO Notice of Allowance for 10/238,966 (CD01250) dated 01/27/09; 4 pages	<input type="checkbox"/>

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/D.C./	2	USPTO Non-Final Rejection for 10/238,966 (CD01250) dated 06/30/08; 12 pages	<input type="checkbox"/>
/D.C./	3	USPTO Non-Final Rejection for 10/238,966 (CD01250) dated 12/26/07; 12 pages	<input type="checkbox"/>
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/D.C./	6	USPTO Non-Final Rejection for 10/238,966 (CD01250) dated 10/20/06; 8 pages	<input type="checkbox"/>
/D.C./	7	USPTO Non-Final Rejection for 10/238,966 (CD01250) dated 04/06/06; 8 pages	<input type="checkbox"/>
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/D.C./	9	USPTO Non-Final Rejection for 10/033,027 (CD00232) dated 12/18/08; 5 pages	<input type="checkbox"/>
/D.C./	10	USPTO Final Rejection for 10/033,027 (CD00232) dated 06/08/07; 8 pages	<input type="checkbox"/>
/D.C./	11	USPTO Non-Final Rejection for 10/033,027 (CD00232) dated 12/21/06; 31 pages	<input type="checkbox"/>
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/D.C./	13	USPTO Non-Final Rejection for 10/033,027 (CD00232) dated 04/26/06; 26 pages	<input type="checkbox"/>
/D.C./	14	USPTO Final Rejection for 10/033,027 (CD00232) dated 10/31/05; 24 pages	<input type="checkbox"/>
/D.C./	15	USPTO Non-Final Rejection for 10/033,027 (CD00232) dated 04/20/05; 20 pages	<input type="checkbox"/>
/D.C./	16	USPTO Non-Final Rejection for 10/033,027 (CD00232) dated 10/18/04; 17 pages	<input type="checkbox"/>
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/D.C./	29	USPTO Final Rejection for 10/002,217 (CD01207) dated 02/06/08; 10 pages	<input type="checkbox"/>
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/D.C./	35	USPTO Non-Final Rejection for 10/002,217 (CD01207) dated 05/19/05; 15 pages	<input type="checkbox"/>
/D.C./	36	USPTO Notice of Allowance for 10/001,477 (CD01203) dated 11/10/08; 7 pages	<input type="checkbox"/>
/D.C./	37	USPTO Advisory Action for 10/001,477 (CD01203) dated 10/10/08; 3 pages	<input type="checkbox"/>
/D.C./	38	USPTO Final Rejection for 10/001,477 (CD01203) dated 06/30/08; 15 pages	<input type="checkbox"/>
/D.C./	39	USPTO Non-Final Rejection for 10/001,477 (CD01203) dated 12/06/07; 13 pages	<input type="checkbox"/>
/D.C./	40	USPTO Final Rejection for 10/001,477 (CD01203) dated 07/23/07; 16 pages	<input type="checkbox"/>
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Examiner Signature	/Dwin Craig/	Date Considered	10/13/2011
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/D.C./	9	6249167	B1	2001-06-19	Oguchi et al.	Entire Document
/D.C./	10	4134073	B1	1979-01-01	MacGregor	Entire Document
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/D.C./	13	5371860	B1	1994-12-06	Mura et al.	Entire Document
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/D.C./	20	5331571	B1	1994-07-19	Aronoff et al.	Entire Document
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/D.C./	31	5428319	B1	1995-06-27	Marvin et al.	Entire Document
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/D.C./	42	4947169	B1	1990-08-07	Smith et al.	Entire Document
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/D.C./	47	5440305	B1	1995-08-08	Signore et al.	Entire Document
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/D.C./	1	USPTO Non-Final Rejection for 09/975,115 (CD00182) dated 10/09/08; 34 pages	<input type="checkbox"/>
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First Named Inventor	Craig Nemecek
Art Unit	2123
Examiner Name	Jason Scott Prester /Dwin Craig/
Attorney Docket Number	CD01213M

/D.C./	28	USPTO Notice of Allowance for 10/008,096 (CD01212M) dated 12/22/08; 24 pages	<input type="checkbox"/>
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	33	Application Number 09/943,062 (CD00228): "Apparatus and Method for Programmable Power Management in a Programmable Analog Circuit Block," Mar, 46 pages	<input type="checkbox"/>
	34	Application Number 10/238,966 (CD01250M): "Method for Parameterizing a User Module," Pemini, 41 pages	<input type="checkbox"/>
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Examiner Initial*	Cite No	Patent Number	Kind Code ¹	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
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/D.C./	2	5399922	B1	1995-03-21	Kiani et al.	Entire Document
/D.C./	3	5680070	B1	1997-10-21	Anderson et al.	Entire Document
/D.C./	4	6314530	B1	2001-11-06	Mann	Entire Document
/D.C./	5	5438672	B1	1995-08-01	Dey	Entire Document
/D.C./	6	6166367	B1	2000-12-26	Cho	Entire Document
/D.C./	7	5600262	B1	1997-02-04	Kolze	Entire Document
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/D.C./	9	5258760	B1	1993-11-02	Moody et al.	Entire Document
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8	104083405	JP	A1	1992-03-17	Yasue	Entire Document	<input type="checkbox"/>	
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/D.C./	1	From Application Number 10/033,027 (CD00232); Goodenough, F. "Analog Counterparts of FPGAS Ease System Design" Electronic Design, Penton Publishing, Cleveland, OH, US vol. 42, no. 21, 10/14/94; 10 pages	<input type="checkbox"/>
/D.C./	2	From Application Number 10/033,027 (CD00232); Harbaum, T. et al. "Design of a Flexible Coprocessor Unit" Proceedings of the Euromicro Conference, XX XX, Sept. 1999; 10 pages	<input type="checkbox"/>
/D.C./	3	From Application Number 10/033,027; CD00169: "Programmable Microcontroller (PSoC) Architecture (Mixed Analog/Digital)"; 08/07/01; 09/924,734 Snyder et al.; 28 pages	<input type="checkbox"/>
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		Attorney Docket Number	CD01213M

/D.C./	5	From Application Number 10/033,027: CD00172; "Configuring Digital Functions in a Digital Configurable Macro Architecture"; 07/18/01; 09/909,109; Snyder; 38 pages	<input type="checkbox"/>
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/D.C./	8	From Application Number 10/033,027: CD00175; "Method for Synchronizing and Resetting Clock Signals Supplied to Multiple Programmable Analog Blocks (As Amended)"; 10/01/01; 09/969,311; Sullam; 57 pages	<input type="checkbox"/>
/D.C./	9	From Application Number 10/033,027: CD00180; "Method and Apparatus for Programming a Flash Memory"; 06/05/01; 09/875,599; Snyder; 23 pages	<input type="checkbox"/>
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/D.C./	19	HINTZ ET AL.; "MICROCONTROLLERS", 1992, McGraw-Hill, 11 pages	<input type="checkbox"/>
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	26	Application Number 09/072,319 (CD00179); "Method for Applying Instructions to Microprocessor in Test Mode;" Snyder; 31 pages	<input type="checkbox"/>

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27	Application Number 09/972,003 (CD00176): "Test Architecture for Microcontroller Providing for a Serial Communication Interface;" Snyder; 32 pages	<input type="checkbox"/>
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38	Application Number 10/004,039 (CD01222M): "In-Circuit Emulator with Gatekeeper for Watchdog Timer;" Nemecek; 46 pages	<input type="checkbox"/>
39	Application Number 10/002,217 (CD01207M): "Conditional Branching in an In-Circuit Emulation System;" Nemecek; 43 pages	<input type="checkbox"/>
40	Application Number 10/001,568 (CD01214M): "Combined In-Circuit Emulator and Programmer;" Nemecek; 47 pages	<input type="checkbox"/>
41	Application Number 10/011,214 (CD00131): "Method and Circuit for Synchronizing a Write Operation between an On-Chip Microprocessor and an On-Chip Programmable Analog Device Operating at Different Frequencies;" Sullam; 49 pages	<input type="checkbox"/>
42	Application Number 09/887,955 (CD00197): "Novel Power on Reset Circuit for Microcontroller;" Kutz; 42 pages	<input type="checkbox"/>
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	14	Sedory; "A Guide to Debug"; 2004; retrieved on May 20, 2005; 7 pages	<input type="checkbox"/>
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	18	Wikipedia.org; "Von Neumann architecture"; retrieved from http://en.wikipedia.org/wiki/Von_Neumann_architecture on January 22, 2007; 4 pages	<input type="checkbox"/>
	19	Stan Augarten; "The Chip Collection - Introduction - Smithsonian Institute"; "State of the Art"; "The First 256-Bit Static RAM"; retrieved on November 14, 2005 from http://smithsonianchips.si.edu/augarten/p24.htm ; 2 pages	<input type="checkbox"/>
	20	"POD - Piece of Data, Plain Old Documentation, Plain Old Dos..."; retrieved on November 14, 2005 from http://www.auditmypc.com/acronym/POD.asp ; 2 pages	<input type="checkbox"/>
	21	USPTO Non-Final Rejection for 09/989,767 (CD01178M) dated 10/06/04; 15 pages	<input type="checkbox"/>
/D.C./	22	USPTO Final Rejection for 09/989,771 (CD01182) dated 02/27/07; 8 pages	<input type="checkbox"/>

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
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Application Number	10001478	
Filing Date	2001-11-01	
First Named Inventor	Craig Nemecek	
Art Unit	2123	
Examiner Name	Jesse Scott Preter	/Dwin Craig/
Attorney Docket Number	CD01213M	

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	26	USPTO Final Rejection for 09/989,771 (CD01182) dated 12/27/07; 15 pages	<input type="checkbox"/>
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▼	32	USPTO Final Rejection for 09/989,765 (CD01194M) dated 04/03/07; 12 pages	<input type="checkbox"/>
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Not for submission under 37 CFR 1.99)</i>		Application Number	10001478
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		Attorney Docket Number	CD01213M

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Attorney Docket Number	CD01213M

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/D.C./	47	USPTO Non-Final Rejection for 09/989,782 (CD01196M) dated 11/26/08; 10 pages	<input type="checkbox"/>

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/D.C./	10	5241492	B1	1993-08-31	Girardeau Jr.
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	12	6453461	B1	2002-09-17	Chaiken
	13	6449755	B1	2002-09-10	Beausang, et al.
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/D.C./	2	"pod-definition by dict.die.net"; retrieved on November 14, 2005 from http://dict.die.net/pod ; 2 pages	<input type="checkbox"/>
	3	"In-Circuit Emulators - descriptions of the major ICES around"; retrieved on November 14, 2005 from http://www.algonet.se/~staffan/developer/emulator.htm ; 6 pages	<input type="checkbox"/>
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Attorney Docket Number	CD01213M

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/D.C./	6	Anonymous; "Using Debug"; 1999; Prentice-Hall Publishing; 20 pages	<input type="checkbox"/>
/D.C./	7	Harrison et al.; "Xilinx FPGA Design in a Group Environment Using VHDS and Synthesis Tools"; Colloquium on Digital System Design Using Synthesis Techniques; February 15, 1996; 4 pages	<input type="checkbox"/>
/D.C./	8	Microsoft Press Computer User's Dictionary; 1998; 3 pages	<input type="checkbox"/>
/D.C./	9	Sreeram Duvvuru and Siamak Arya, "Evaluation of a Branch Target Address Cache," 1995; IEEE; 8 pages	<input type="checkbox"/>
/D.C./	10	Andrew S. Tanenbaum with contributions from James R. Goodman, "Structured Computer Organization," 1999, Prentice Hall, Fourth Edition; 32 pages	<input type="checkbox"/>
/D.C./	11	USPTO Non-Final Rejection for 09/989,767 (CD01178M) dated 07/17/06; 12 pages	<input type="checkbox"/>
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Attorney Docket Number	CD01213M

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Attorney Docket Number	CD01213M

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/D.C./	38	USPTO Final Rejection for 09/998,848 (CD01177M) dated 11/24/08; 15 pages	<input type="checkbox"/>
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↑	2	6002398	B1	1999-12-14	Wilson, Daniel	Entire Document
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Attorney Docket Number	CD01213M	

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	10	6384947	B1	2002-05-07	Ackerman, et al.	Entire Document
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(Not for submission under 37 CFR 1.99)

Application Number	10001478
Filing Date	2001-11-01
First Named Inventor	Craig Nemecek
Art Unit	2123
Examiner Name	Jason Scott Preeter /Dwin Craig/
Attorney Docket Number	CD01213M

/D.C./	1	USPTO Notice of Allowance for 09/992,076 (CD01210M) dated 11/13/2008; 15 pages	<input type="checkbox"/>
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		Attorney Docket Number	CD01213M				

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/D.C./	1	20030041235	A1	2003-02-27	Meyer	Entire Document
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First Named Inventor	Craig Nemecek	
Art Unit	2123	
Examiner Name	Jason Scott Prester /Dwin Craig/	
Attorney Docket Number	CD01213M	

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Art Unit	2123
Examiner Name	Jean Scott Brooks /Dwin Craig/
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Application Number	10001478
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First Named Inventor	Craig Nemecek
Art Unit	2123
Examiner Name	Jason Scott Prester /Dwin Craig/
Attorney Docket Number	CD01213M

/D.C./	28	USPTO Non-Final Rejection for Application Number 09/972,133 (CD00178) dated 11/25/2005; 9 pages	<input type="checkbox"/>
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U.S.PATENTS						
Examiner Initial*	Cite No	Patent Number	Kind Code ¹	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
/D.C./	1	6934674	B1	2005-08-23	Douzeby et al.	Entire Document
/D.C./	2	6051772	B1	2000-04-18	Cameron et al.	Entire Document
/D.C./	3	6016554	B1	2000-01-18	Skrovan et al.	Entire Document
/D.C./	4	6704889	B1	2004-03-09	Veenstra et al.	Entire Document
/D.C./	5	5978937	B1	1999-11-02	Miyamori et al.	Entire Document
/D.C./	6	5317202	B1	1994-05-31	Waizman	Entire Document
/D.C./	7	6121805	B1	2000-09-19	Thamsirianunt et al.	Entire Document
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/D.C./	9	6396302	B1	2002-05-28	New et al.	Entire Document
/D.C./	10	6204687	B1	2001-03-20	Schutlz et al.	Entire Document
/D.C./	11	5996032	B1	1999-11-30	Baker	Entire Document
/D.C./	12	5608892	B1	1997-03-04	Wakerly	Entire Document
/D.C./	13	5808883	B1	1998-09-15	Hawkes	Entire Document
/D.C./	14	5949632	B1	1999-09-07	Barreras Sr. et al.	Entire Document
/D.C./	15	6542025	B1	2003-04-01	Kutz et al.	Entire Document
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/D.C./	17	5894243	B1	1999-04-13	Hwang	Entire Document
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/D.C./	20	6286127	B1	2001-09-04	King et al.	Entire Document
/D.C./	21	6166960	B1	2000-12-26	Marneweck et al.	Entire Document
/D.C./	22	5982105	B1	1999-11-09	Masters	Entire Document
/D.C./	23	4740966	B1	1988-04-26	Goad	Entire Document
/D.C./	24	6643151	B1	2003-11-04	Nebrigic et al.	Entire Document
/D.C./	25	6003133	B1	1999-12-14	Moughanni et al.	Entire Document
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/D.C./	27	6574739	B1	2003-06-03	Kung et al.	Entire Document
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/D.C./	29	5969632	B1	1999-10-19	Diamant et al.	Entire Document
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/D.C./	31	6839774	B1	2005-01-04	Ahn et al.	Entire Document
/D.C./	32	6754765	B1	2004-06-22	Chang et al.	Entire Document
/D.C./	33	5630057	B1	1997-05-13	Hait	Entire Document
/D.C./	34	6324628	B1	2001-11-27	Chan	Entire Document
/D.C./	35	6236593	B1	2001-05-22	Hong et al.	Entire Document
/D.C./	36	6219729	B1	2001-04-17	Keats et al.	Entire Document
/D.C./	37	5319771	B1	1994-06-07	Takeda	Entire Document
/D.C./	38	5378935	B1	1995-01-03	Korhonen et al.	Entire Document
/D.C./	39	5572719	B1	1996-11-05	Blesterfeldt	Entire Document
/D.C./	40	5579353	B1	1996-11-26	Parmenter et al.	Entire Document
/D.C./	41	5696952	B1	1997-12-09	Pontarelli	Entire Document

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Attorney Docket Number	CD01213M	

/D.C./	42	5774704	B1	1998-06-30	Williams	Entire Document
/D.C./	43	5986479	B1	1999-11-16	Mohan, Jitendra	Entire Document
/D.C./	44	6603348	B1	2003-08-05	Preuss et al.	Entire Document

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/D.C./	2	20020059543	A1	2002-05-16	Cheng et al.	Entire Document
/D.C./	3	20010010083	A1	2001-07-26	Satoh	Entire Document
/D.C./	4	20020133794	A1	2002-09-19	Kanapathippillai et al.	Entire Document
/D.C./	5	20020065646	A1	2002-05-30	Waldie et al.	Entire Document
/D.C./	6	20020133771	A1	2002-09-19	Barnett	Entire Document

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Examiner Name	Jason Scott Proctor	/Dwin Craig/
Attorney Docket Number	CD01213M	

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First Named Inventor	Craig Nemecek
Art Unit	2123
Examiner Name	Jason Scott Reeder /Dwin Craig/
Attorney Docket Number	CD01213M

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/D.C./	2	6373954	B1	2002-04-16	Malcolm et al.	Entire Document
/D.C./	3	5900780	B1	1999-05-04	Hirose et al.	Entire Document
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/D.C./	6	5248843	B1	1993-09-28	Billings	Entire Document
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	13	Application Number 11/644,100 (CD05204); "Differential-to-single-ended signal converter circuit and method," Srinivasan, 33 pages	<input type="checkbox"/>
	14	Application Number 11/415,588 (CD05028); "Voltage Controlled Oscillator Delay Cell and Method," Srinivasan, 24 pages	<input type="checkbox"/>
	15	Application Number 12/218,404 (CD05028C1); "Voltage Controlled Oscillator Delay Cell and Method," Srinivasan, 28 pages	<input type="checkbox"/>
/D.C./	16	USPTO Notice of Allowance for Application Number 11/132,894 (CD04064) dated 04/26/2007; 4 pages	<input type="checkbox"/>

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Examiner Name	Jason Scott Proctor /Dwin Craig/
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/D.C./	17	USPTO Non-Final Rejection for Application Number 11/132,894 (CD04064) dated 12/19/2006; 12 pages	<input type="checkbox"/>
	18	Application Number 11/132,894 (CD04064): "Open Loop Bandwidth Test Architecture and Method for Phase Locked Loop (PLL)," Stiff; 38 pages	<input type="checkbox"/>
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/D.C./	21	USPTO Non-Final Rejection for Application Number 11/322,044 (CD05198) dated 04/11/2008; 11 pages	<input type="checkbox"/>
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	25	Application Number 11/322,044 (CD05198): "Split charge pump PLL architecture," Stiff; 10 pages	<input type="checkbox"/>
/D.C./	26	USPTO Notice of Allowance for Application Number 10/305,589 (CD02181) dated 02/04/2005; 5 pages	<input type="checkbox"/>
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/D.C./	28	USPTO Non-Final Rejection for Application Number 10/305,589 (CD02181) dated 10/07/2003; 6 pages	<input type="checkbox"/>
	29	Application Number 10/305,589 (CD02181). "Current Controlled Delay Circuit," Off, 10 pages	<input type="checkbox"/>
	30	Application Number 09/849,164 (CD04035). "Reduced Static Phase Error CMOS PLL Charge Pump," Off, 30 pages	<input type="checkbox"/>
/D.C./	31	Maneatis, "Low-Jitter Process-Independent DLL and PLL Based on Self-Biased Techniques," IEEE Journal of Solid-State Circuits, Vol. 31, No. 11, November 1996; 10 pages	<input type="checkbox"/>
/D.C./	32	Larsson, "A 2-1600-MHz CMOS Clock Recovery PLL with Low-V _{dd} Capability," IEEE Journal of Solid-State Circuits, Vol. 34, No. 12, December 1999; 10 pages	<input type="checkbox"/>
/D.C./	33	USPTO Notice of Allowance for Application Number 10/327,217 (CD02174) dated 08/12/2004; 6 pages	<input type="checkbox"/>
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/D.C./	35	USPTO Miscellaneous Action for Application Number 10/327,217 (CD02174) dated 02/10/2004; 1 page	<input type="checkbox"/>
	36	Application Number 10/327,217 (CD02174): "Single Ended Clock Signal Generator Having a Differential Output," Richmond et al., 27 pages	<input type="checkbox"/>
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/D.C./	38	USPTO Final Rejection for Application Number 10/871,582 (CD03036) dated 02/01/2006; 5 pages	<input type="checkbox"/>

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↑	10	6597212	B1	2003-07-22	Wang et al.	Entire Document
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↑	21	6704381	B1	2004-03-09	Moyal et al.	Entire Document
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/D.C./	35	6949984	B1	2005-09-27	Siniscalchi	Entire Document
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/D.C./	45	7015735	B1	2006-03-21	Kimura et al.	Entire Document
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/D.C./	48	6937075	B1	2005-08-30	Lim et al.	Entire Document
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	1	Application Number 10/226,911 (CD02112); "Calibration of Integrated Circuit Time Constants," Gehrung et al., 02 pages	<input type="checkbox"/>
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	6	Application Number 09/943,149 (CD01085); "Method for Phase Locking in a Phase-Lock Loop," Moyal et al., 24 pages	<input type="checkbox"/>
/D.C./	7	Durham et al., "Integrated Continuous-Time Balanced Filters for 16-bit DSP Interfaces," IEEE, 1993; 6 pages	<input type="checkbox"/>

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	10	Application Number 09/047,595 (CD97115): "Roving Range Control to Limit Receive PLL Frequency of Operation." Scott; 35 pages	<input type="checkbox"/>
	11	Application Number 09/216,460 (CD98099): "Circuit and Method for Controlling an Output of a Ring Oscillator." Abugharbieh et al.; 21 pages	<input type="checkbox"/>
	12	Application Number 09/471,914 (CD99064): "Reference-Free Clock Generator and Data Recovery PLL." Dalmia et al.; 32 pages	<input type="checkbox"/>
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	14	Application Number 10/083,442 (CD01264): "Method/Architecture for a Low Gain PLL with Wide Frequency Range." Meyers et al.; 28 pages	<input type="checkbox"/>
	15	Application Number 09/470,665 (CD99065): "Digital Phase/Frequency Detector, and Clock Generator and Data Recovery PLL Containing the Same," Dalmia; 26 pages	<input type="checkbox"/>
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	17	Application Number 09/608,753 (CD00018): "PLL Lockout Watchdog." Wilson et al.; 24 pages	<input type="checkbox"/>
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20	Application Number 09/904,140 (CD01102); "Oscillator Tuning Method," Hauck; 26 pages		<input type="checkbox"/>
21	Application Number 09/538,000 (CD00104); "Memory-Based Phase-Locked Loop," Krishnan; 27 pages		<input type="checkbox"/>
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		Art Unit		2123	
		Examiner Name		Jason Scott Preeter /Dwin Craig/	
		Attorney Docket Number		CD01213M	

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		Attorney Docket Number	CD01213M

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/D.C./	6	USPTO Non-Final Rejection for Application Number 11/201,922 (CD04111) dated 10/21/2008; 12 pages	<input type="checkbox"/>
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(Not for submission under 37 CFR 1.99)

Application Number	10001478
Filing Date	2001-11-01
First Named Inventor	Craig Nemecek
Art Unit	2123
Examiner Name	Jason Scott Prester /Dwin Craig/
Attorney Docket Number	CD01213M

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Examiner Signature	/Dwin Craig/	Date Considered	10/13/2011
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First Named Inventor	Craig Nemecek
Art Unit	2123
Examiner Name	Jason Scott Proctor /Dwin Craig/
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Examiner Name	Jason Scott Foote	/Dwin Craig/
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